Design And Construction Of Temperature-Controlled Fan

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December, 2011

DESIGN AND CONSTRUCTION OF TEMPERATURE-CONTROLLED

FAN

NATIONAL DIPLOMA PROJECT REPORT

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SUBMITTED TO THE DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING TECHNOLOGY, NUHU BAMALLI POLYTECHNIC IN PARTIAL FULFILLMENT OF THE REQUIREMENT FOR THE AWARD OF NATIONAL DIPLOMA

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SCHOOL OF ENGINEERING TECHNOLOGY

NUHU BAMALLI POLYTECHNIC, ZARIA

DECEMBER 2011

DECLARATION

I here by declare that this project has been conducted solely by me under guidance of Malam Abubakar Mujahid (H.O.D) Department of Electrical Electronics Engineering Technology, Nuhu Bamalli Polytechnic, Zaria and I have neither copied some one's work nor has someone else done it for me. Authors whose works have been refereed to in this project have been duly acknowledge

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Date 22/1/2012

APPROVAL PAGE

This is to certify that this is an original work undertaken by ABUBAKAR ALIYU, N/EEET/09/27 and has been prepared in accordance with the regulations governing and presentation of projects in Nuhu Bamalli Polytechnic, Zaria.

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DEDICATION

This project is dedicated to my dear parent Alh. Aliyu Yahaya Abubakar and Haj. Fatimah Aliyu. I also dedicated this project to my lovely brothers and sisters for their maximum support and corporation toward the achievement of my education.

ACKNOWLEDMENT

I firstly express my paramount gratitude to the Almighty Allah for His guidance, protection and provision through out my studies and my life from the childhood.

! also extend my regards and thanks to my beloved parent Alh. Aliyu Yahaya and Haj. Fatimah Aliyu for their financial and moral support during my studies. My thanks goes to my brothers and sisters whose names are: Shehu Aliyu, Sani Aliyu, Abdullahi Aliyu, Alh. Musa Aliyu, Haj. Amina Aliyu, Salamatu Aliyu, Maimuna Aliyu, and the rest which the time may not allow me to mention them.

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ABTRACT

The aim of this project is to design and construct a temperature controlledfan. The temperature in general can play a vital role to the human used, which man can use for many purposes. That is to say man can control a temperature for the achievement of many things but here is to use it as fan controlled.

The fan control unit was designed and constructed. The principle was based on creating a potential difference in a Wheatstone bridge by using a potentiometer. The transistor Q1 is used as a bridge balance detector. This mean as the temperature increase the speed of the fan will increase proportionally.

CHAPTER ONE

1.0 INTRODUCTION

In ancient times temperature control of substance was not accurately achieved despite the fact that human and natural recourses were available to do it. This circuit adopt a rather old design technique as its purpose is to vary the speed of a fan related to temperature with a minimum parts counting and avoiding the use of special purpose IC's often difficult to obtain.

Over centuries man had distinguished himself by the resources he had shown using national energy to enable him accomplish tasks beyond his own physical power.

Based on this background the project was centered to design a circuit to control a fan when the temperature is high. The circuit consist of sensing element (i.e Thermistor) which senses the temperature. The zener diode which act as an amplifier and the silicon controlled rectifier (SCR) or thyristor which can control the power supplied to a load and at the out put of diode bridge with out a smoothing capacitor.

1.1 PRINCIPLE OF OPERATION

R3-R4 and P1-R1 are wired as a Wheatstone bridge which R3-R4 generate a fixed two thirds-supply reference voltage, P1-R1 generate a temperature sensitive "variable" voltage, and transistor is used as a bridge balance detector.

P1 is adjusted so that the "reference" and "variable" voltage are equal at a temperature just below the required trigger value and under this condition the

transistor Q1 base and Emitter are at equal voltages when the R1 temperature goes above this "balance" value the P1-R1 voltage falls below the "reference" value, so Q1 becomes forward biased of equal charging C1. C1 provides a variable phase delay pulse-train related to temperature and synchronous with the mains supply "Zero Voltage" point of each half cycle.

CHAPTER TWO

2.0 LITERATURE REVIEW

TRANSDUCSER: This is an electrical device which gives an output of a form easily usable in response to a particular property being measured. The3 form of the output from a transducer may not be directly usable by our senses, for examples. A microphone changes sound energy in to electrical energy unless a loud speaker changes this electrical energy in to sound energy. The microphone output may not be usable to our senses.

Some transducers are used for the control of temperature against our heating. Some are used to maintain a constant temperature over aide range some forms of transducers includes:

a) THERMISTOR (b) PLATUM (c) TERMOSTATE

2.1 THERMISTORS

Fig 2.1

A thermistor is a semi-conductor transducer whose resistance changes markedly when its temperature changes. These are in varieties as shown below:

d

THERMISTOR CONFIGURATION

a. Bead type b. Disk type c. Washer type d. Rod type

THE NAME TERMISTOR IS DERIVED FROM THERMALY SENSITIVE RESISTORS.

Resistance of most thermistors decreases as their temperature increase and these are the NTC type negative temperature coefficient made from oxide of nickel, manganese, copper, cobalt and other materials.

They are used for temperature control and measurements. They are heated either externally, from the surrounding or by current flowing through them. Resistance changes causes current or voltage changes which supply the input.

With P.T.C positive type thermistor, the resistance increases as the temperature increases. These are based on barton titanate and are used mainly TO PREVENT damage in circuit which might experience a large temperature rise. This can happen to a much overloaded electric motors.

The resistance of the thermistor once manufactured and fired is dependant solely upon it's temperature.

R= Ae b/T

Where R is Resistance of thermistor

A,b are constant

e: Base of natural logarithm

T: Absolute Temperature.

2.2 ZENER DIODE: We know that in an ordinary junction diode, if the reverse biases is increased until the depletion layer brakes down. The diode supper's permanent damage.

Therefore a zener diode is made to be uded in the breakdown region. So by as resistor limit the current.

STRUCTRURE: IT LOOKS LIKE A RECTIFIERE DIODE, THE Cathode often being marked by a band. As shown below: fig 2.2



CHARACTERISTICS: Variation of the characteristics is by temperature. The Vz value of zener diode is variable with the temperature. It occurs that

- In a diode which has a zener voltage Vz < 5v, the decreases as the temperature increases.
- 2. In a diode which has a voltage Vz > 5v the Vz increases as the temperature increases. And diode with Vz -5v is thermally more stable. Important thing about diode is that voltage across the diode remains almost constant over a wide range of reverse current.

Therefore based on the property, it is useful in stabilized power supply where it keeps the voltage output steady. Also zener diode is a circuit element capable of withstanding considerable current variation while maintain its constant voltage at its terminals. For the purpose of construction and to limit the reverse current at breakdown and to prevent over heating power rating of diode should be carefully being exceeded.

TRANSISTOR AS SWITCH



Fig 2.3 basic circuit of a transistor as a switch.

The off state is obtained by reverse biasing. The Base- Emitter junction so that the base current IB is negligible. In this condition, the transistor does not conduct only the collector leakage current Ico flows.

In this condition, almost the whole supply drop across the transistor with no supply drop across the load.

The on state is obtained by forward biasing the base Emitter junction such that Ib is large enough to drive the transistor into saturation under this condition, collector current Ic (SAT) is obtained from the expression given below:

Ic (sat) = Vcc - Vce/ Rse (sat)

The minimum current required to saturate a transistor i.e trigger to on (state) is expressed below.

Ib = Ic/hfe SAT

Where Ic (SAT) = current gain of the transistor at the ON state saturation

6

hpe = common-Emitter current gain

The ON and OFF stage can be illustrated by fig 1.3 which show the plot of Ic



2.4 TWO TRANSISTOR ANALOGY

The basic operation of a SCR can be described by using two transistor analogy. For this purpose, SCR as split in to 3-layer transistor structures as shown in fig.1.2 (a) As seen transistor Q2 is a PNP transistor where as Q3 is an NPN device interconnected together. It will also be noted from fig.1.2 (b) that (i) collector current of Q2 is also the collector current of Q3 and (ii) base current of Q3 is also the collector current of Q2.

If the supply voltage across terminals A and C is reverse-biased junction. Then the current through the device will raise. It means le is increase.

- Ics increase (remember Ic = IE
- Since Ics= Ib2, Ib2 also increases
- lc2 increase (remember lc2 =Bla)
- Now, Ic2 = Ib, hence Ib increase

It can be proved that if IG is the gate current of the SCR and 1 and 2, the current gains of the PNP and NPN transistors respectively than anode current is given by

....



Fig 1.2 (a)

d

LITERATURE REVIEW OF SILICON CONTROLLED RECTIFIER (SCR)

2.5 THYRISTOR: Is a four-layer, three terminal semiconductor device as shown in fig.1.3 (a) , (b) used to be called silicon controlled rectifier (SCR) it is a rectifier which can control the power supplied to a load and in a way that with very energy.





When forward biased, a thyristor does not conduct until a positive voltage is applied to the gate, conduction continues when the gate voltage is removed and stops only if the supply voltage is switched OFF or reversed or the anode current falls below a certain values.

The control of A.C power can be achieved by allowing current to be supplied to the load during only part of each cycle.

A gate pulse is applied automatically at a certain chosen stage during each Positive half cycle of input. This allows the thyristor conduct and the load receives Power.

Show below in fig 2.7 the pulse occurs at the peak of the A.C input.



Switching device to the circuit.

CHAPTER THREE

3.0 DESIGN

3.1 DESIGN OF A THERMISTOR BRIDGE

The aim of using the Wheatstone bridge is to measure the resistance of the thermistor. This method of resistance measurement is employ for the purpose of accuracy.



Fig 3.1

R4 is a fixed value resistance; R1 is the resistance of the thermistor (15k Ω) at room temperature. P1 and R3 are linear resistance potential, R4 and P1 are set to and arbitrary value and R3 vary at random until a positive error voltage i.e signal or about (2.0v) appears at V1, when the temperature ld increasing from room temperature. And R1 is used as a bridge detector as shown below

the thermistor bridge can be represented as shown in fig. 3.2 for the designing choosing 230va.c



Fig 3.2

Let the current through R1,R2,R3 and R4 be 15.3mA, 2.3mA, 23mA and 10.45mA respectively.

$$\begin{split} & \mathsf{R}_1 = \mathsf{V}/\mathsf{I}_1 = 230/15.3 \times 10^{-3} = 15032.6\Omega \\ & \mathsf{P}_1 = \mathsf{I}_1^{-2}.\mathsf{R}_1 = (15.3 \times 10^{-3})^{-2} \times 15032.6 = 3.52 \text{ w} (1/4 \text{ w preferred value}) \\ & \mathsf{R}_3 = \mathsf{V}/\mathsf{I}_3 = 23/23 \times 10^{-3} = 10000\Omega \\ & \mathsf{P}_3 = \mathsf{I}_3^2\mathsf{R}_3 = (23 \times 10^{-3})^2 10000 = 5.29 \text{ w} (1/4 \text{ w preferred}). \\ & \mathsf{R}_4 \mathsf{V}/\mathsf{I}_4 = 230/10.45 \times 10^{-3} = 22009.57\Omega \\ & \mathsf{P}_4 = \mathsf{I}_4^2\mathsf{R}_4 = (10.45 \times 10^{-3})^2 22009.57 = 2.4 \text{ w} (1/4 \text{ preferred}) \\ & \mathsf{P}_1 \text{ as shown } 22 k\Omega \end{split}$$

 $R_2 = V/I_2 = 230/2.3 \times 10^{-3} = 100,000 \Omega$

 $P_2 = |_2^2 R_2 = (2.3 \times 10^{-3})^2 \ 100000 = 0.53 \text{ w} (1/4 \text{ preferred})$

TWO TRANSISTOR ANALOGY

3.2

The figure below represent the two transistor analogy



A brief discussion on the transistor analogy is given in the literature review. Here will be concerned with the design in respect to this the following assumption are:

Gain of PNP transistor () = 0.4

Gain of NPN transistor () = 0.5

Gate current (IG) = 5-0mA

Therefore

$$I_{k} = \frac{\sqrt{2}I_{0}}{1 + \sqrt{2}}$$

$$= \frac{0.5 \cdot 50^{*}10^{-3}}{1 - (0.4 + 0.5)} = \frac{0.025}{1 - 0.9}$$

$$= 0.25 = 0.25A = 250mA$$

$$= 0.1$$
Let the resistors of R5,R6,R7,R8 and R9 be 22k, 10k, 100, 470 and 33k all are ohm's (n).

$$I_{5} = V/R_{5} = 230/22^{*}10^{-3} = 10.45mA$$

$$P_{5} = I_{5}^{2}.R_{5} = (10.45^{*}10^{-3})^{2}.22^{*}10^{-3} = 2.4w (1/4w \text{ preferred})$$

$$I_{6} = V/R_{6} = 230/10^{*}10^{-3} = 23mA$$

$$P_{5} = I_{6}^{2}.R_{6} = (23^{*}10^{-3})^{2} \cdot 10^{*}10^{-3} = 5.3w (1/4w \text{ preferred})$$

$$I_{7} = V/R_{7} = 230/470 = 0.49A$$

$$I_{9} = V/R_{9} = 230/33000 = 6.97mA$$

$$P_{9} = I_{9}^{-3}.R_{9} = (6.97^{*}10^{-3})^{2} \cdot 33000 = 1.6w (1/4w \text{ preferred}).$$

in

3.3 CIRCUIT OPERATION

This circuit adopt a rather old design technique as its purpose is to vary the speed of a fan related to temperature with a minimum parts counting and avoiding the use of special purpose ICs, the complete circuit diagram of temperature-controlled fan incorporating all stages used in this project is shown in fig below.

The AC bridge is used with a thermistor (type TH 3VA 1026) as the sensing element.

The linear potentiometer (PI) is adjusted so that the voltage are equal at a temperature just below the required trigger value, and under this condition TR1 Base and Emitter are at equal voltages and TR1 is cut off. When the thermistor temperature goes above this value the P1-R1 voltage falls below the value, so TR1 becomes forward biased.

This occur because the whole circuit is supply from the mains supply by means of D3-D6 diode bridge without a smoothing capacitor and fixed to 18v by R9 and zener diode. The capacitor provides a variable phase-delay pulse related to temperature and synchronous with the main supply "zero voltage" which produced minimum switching REI from the SCR•TR1 and TR2 from a trigger device.

which provides a value in

CIRCUIT DIAGRAM



CHPATER FOUR

CONSTRUCTION

omponents were used before the construction of fan control system before the postruction commenced, preferred values of the discrete components obtained vdesign were collected and mounted on a bread board for testing.

When the complete system was first tested, it was found to be functional. e components were carefully removed and transferred into a Vero board after ich soldering follows.

In order to avoid any damage of components, during soldering, bits of atoes were put on the leads of each components to absorb the excessive heat. at care should be taken to avoid short circuit of the board after soldering.

LISTED BELOW ARE THE TOOLS USED DURING THE CONSTRUCTION

Soldering iron

Soldering lead

Sucker

Long nose pliers

Jumper wires

TESTING:

This was made after the construction. Equipments used for testing are as

Standing fan

í.

Ac mains supply

ii. Thermistor

N. Soldering iron

this action is due to behavior of the thermistor due to increase in temperature read to the increase in resistance

4.3 PACKAGING

The packaging of the device need a sequarish type box with little opening for hot ventilation due to the heat that develops from the soldering iron to the thermistor.

4.4 TYPE OF CASING

A wooden case is safe although metal box is also okay, hut it must be properly isolated to prevent contact and avoid shock. Nut the most reliable casing is plastic

CHAPTER FIVE

5.0 CONCLUSION

The temperature controlled fan system was designed to increases the speed of the fan as temperature increase widely ranges at 100°C.

Therefore performance test carried out according to components specification on the complete project which shows the positive temperature coefficient thermistor as the sensing elements, indicating the high test temperature that can be obtained is 120°C which is higher than the desired value.

This does not however affect its performance. The design and construction of the system was successful since desired result was obtained.

5.1 RECOMMENDATION

The following recommendation for further works on this type of project is

- For effective operation of this project, the thyristor which is silicon controlled Rectifier most control the power supplied to a load at the out put of Diode Bridge and used as a switching device to the circuit.
- 2. To prevent over heating of diode as mention above in chapter two, the power rating of the diode should not be exceeded.

REFERENCE

Science Electronics Lab, by Electronic kits International co. Inc. In the United State of America 1985 BY JORGE L TELIERCIO.

2. Transistor Circuit Techniques Sicrete and Intergraded By G.J. RITCHIE. Second Edition.

3. Electical Engineering Concept and Applications BY. A BRUCE CERISON, DAVID G GISSER.

OWPONENTS CHARACTEIRSTICS 1 THYRISTOR TYPE 2. ZENER DIODE: 400v Vmax **BZX 79** 18V 500mw APPENDIX A 21 TIC 1060 Imax 5A Herbert The

APPENDIX B

PART LIST:

ransistor	TR1 and R2	BC327
	TR3	BC337
Thyristor	SCR	TIC1060
Diode	DZ	BZ X79
Thermistor	тн	TH-3VA 1026
Capacitor	C1	10nf
Resistor	P1	22K
Resistor	R2	100K
Resistor	R3,R6	10K
Resistor	R4,R5	22K
Resistor	R7	100R
Resistor	R8	470R
Resistor	R9	33K